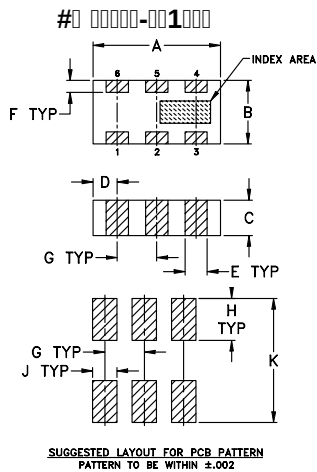


50Ω 650 to 1250 MHz

[illegible]

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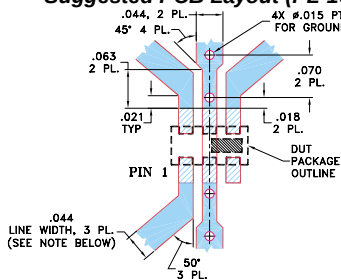


TOLERANCE UNLESS OTHERWISE STATED

2 PLACE DECIMALS: ± 0.01
3 PLACE DECIMALS: ± 0.00

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3.20	1.60	0.94	0.61	0.56	0.30	
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0.99	1.07	0.61	3.12		%6%	

Demo Board MCL P/N: TB-287
Suggested PCB Layout (PL-163)



NOTE: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .020" ± .0015". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

 DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

 DENOTES COPPER LANE PATTERN FREE OF SOLDER MASK

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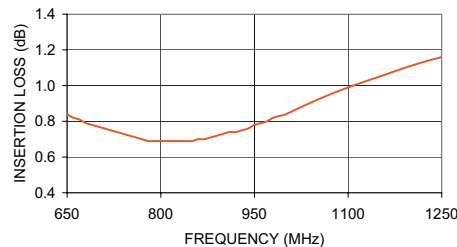
[illegible]

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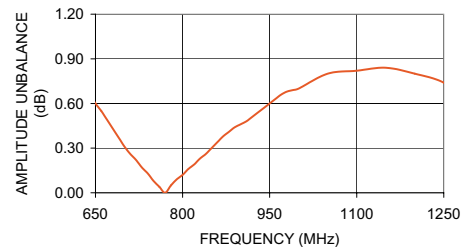
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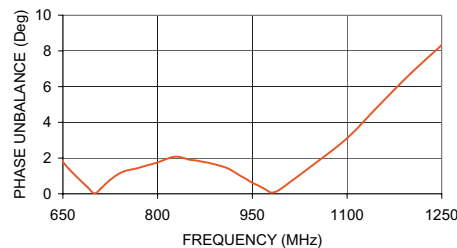
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TCN4-13
INSERTION LOSS

TCN4-13
AMPLITUDE UNBALANCE



TCN4-13
PHASE UNBALANCE



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